

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S6	13	((hardware adj description adj language) HDL) and asynchronous and (((inter adj process) interprocess) adj communication) and clock and TCP and PLD and embedded and debugger	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/20 08:58



"an integrated hardware software cosimulation"

Search

[Advanced Scholar Search](#)
[Scholar Preferences](#)
[Scholar Help](#)

Scholar

Results 1 - 18 of 18 for "an integrated hardware software cosimulation". (0.14 seconds)

An integrated hardware-software cosimulation environment with automated interface generation - group of 6 »

K Kim, Y Kim, Y Shin, K Choi - Proc. of 7th IEEE Int. Workshop on Rapid Systems Prototyping, 1996 - doi.ieeecs.org

Page 1. **An Integrated Hardware-Software Cosimulation Environment with Automated Interface Generation** Kyuseok Kim Yongjoo Kim, Youngsoo ...

Cited by 15 - [Web Search](#)

Integrated hardware-software cosimulation environment for heterogeneous systems prototyping - group of 8 »

Y Kim, K Kim, Y Shin, T Ahn, W Sung, K Choi, S Ha - The 1995 Asia and South Pacific Design Automation Conference ..., 1995 - [ieeexplore.ieee.org](#)

Page 1 101 **An Integrated Hardware-Software Cosimulation Environment for Heterogeneous Systems Prototyping** Yongjoo Kim*, Kyuseok Kim*, Youngsoo Shin*, Taekyoon ...

Cited by 8 - [Web Search](#)

An Integrated Cosimulation Environment for Heterogeneous Systems Prototyping - group of 5 »

Y Kim, K Kim, Y Shin, T Ahn, K Choi - Design Automation for Embedded Systems, 1998 - Springer

Page 1. Design Automation for Embedded Systems 3, 163-186 (1998) c 1998 Kluwer Academic Publishers. Manufactured in The Netherlands. ...

Cited by 8 - [Web Search](#)

Dynamic Communication Models in Embedded System Co-Simulation - group of 12 »

K Hines, G Borriello - DAC, 1997 - doi.ieeecomputersociety.org

Page 1. Dynamic Communication Models in Embedded System Co-Simulation Ken Hines and Gaetano Borriello Department of Computer Science ...

Cited by 65 - [Web Search](#) - [BL Direct](#)

An Integrated Simulation Environment for Parallel and Distributed System Prototyping - group of 4 »

AD George, RB Fogarty, JS Markwell, MD Miars - SIMULATION-CALIFORNIA-, 1999 - hcs.ufl.edu

Page 1. © 1999, HCS Research Lab. All Rights Reserved. An Integrated Simulation Environment for Parallel and Distributed System Prototyping ...

Cited by 9 - [View as HTML](#) - [Web Search](#) - [BL Direct](#)

Automatic VHDL-C Interface Generation for Distributed Cosimulation: Application to Large Design ... - group of 4 »

C Valderrama, F Nacabal, P Paulin, A Jerraya - Design Automation for Embedded Systems, 1998 - Springer ... 20. K. Kim, Y. Kim, Y. Shin, K. Choi, and S. Ha, "An integrated hardware-software cosimulation environment with automated interface generation," in Proc. ...

Cited by 9 - [Web Search](#)

Selective Focus as a Means of Improving Geographically Distributed Embedded System Co-simulation - group of 6 »

K Hines, G Borriello - Proceedings of the 8th International Workshop on Rapid ..., 1997 - cs.washington.edu ... IEEE, ACM, 1997. 5 K. Kim, Y. Kim, Y. Shin, and K. Choi. **An integrated**

hardware-software cosimulation environment with auto- mated interface generation. ...